

Application Serial No. 10/811,768
Reply to Office Action of September 23, 2005

OCT 07 2005

PATENT
Docket: CU-3664**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application: Kiyoshi OOI et al.

Serial No: 10/811,768

Filed: March 29, 2004

For: Semiconductor Device Substrate, Semiconductor Device, and Manufacturing Method Thereof

] GRP ART UNIT: 2891

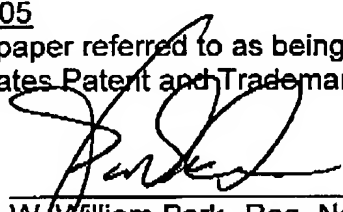
] Ex.: Wilson, Christian D

Certification under 37 C.F.R. §1.8(a)

The USPTO Central Fax No. (571) 273-8300

Date of Fax Transmittal: October 7, 2005

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted to the United States Patent and Trademark Office to the fax number and on the date indicated above.


W. William Park, Reg. No. 55,523

The Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

In the Office Action dated September 23, 2005, setting a 1-month shortened statutory period for a reply ending on October 23, 2005, the pending Claims 1-23 were restricted as follows:

Claims 1-9 drawn to a method of making a semiconductor device, classified in class 438, subclass 118; and

Claims 10-23 drawn to a semiconductor device, classified in class 257, subclass 783.

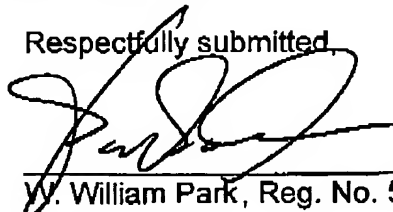
Applicant hereby elects Claims 10-23.

Application Serial No. 10/811,768
Reply to Office Action of September 23, 2005

PATENT
Docket: CU-3664

In view of the Examiner's earlier restriction requirement, Applicants retains the right to present the non-elected in a divisional application.

Respectfully submitted,



Dated: October 7, 2005

W. William Park, Reg. No. 55,523
Ladas & Parry
224 South Michigan Avenue
Chicago, Illinois 60604
(312) 427-1300